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Field of the Invention The present invention relates to an array substrate and a method of manufacturing the same, and a liquid crystal display (“LCD”) device. More particularly, the present invention relates to an array substrate having a reduced number of masking processes and a method of manufacturing the same, and

an LCD device having a reduced number of masking processes. 2. Description of the Related Art A thin film transistor (“TFT”) may be formed on a substrate in various ways. One method involves forming an amorphous silicon (“ α -Si”) thin film on the substrate and crystallizing the α -Si thin film using a laser beam, and a crystallized thin film may have a superior electrical characteristic. Particularly, the electrical characteristic of a poly-Si thin film is similar to that of a field effect transistor (“FET”) formed on an integrated circuit. However, a poly-Si thin film is generally formed by a chemical vapor deposition (“CVD”) process, and the CVD process requires a relatively high-temperature process, making it difficult to use the CVD process in the manufacturing of a TFT-LCD. Also, the CVD process may cause problems with the uniformity of the resulting thin film

and the integrity of a substrate. Thus, a method of manufacturing a TFT using an α -Si thin film formed by a low-temperature process has recently been under investigation, and the α -Si thin film may be formed using a photo-etching process, which makes it easier to apply to an LCD. However, compared to the CVD process, the α -Si thin film requires a relatively high-temperature process f678ea9f9e

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